

IN THE SPECIFICATION

Please amend paragraph [0014] of page 7 as indicated below.

[0014] For purposes of this specification a primary PCI bus 150 may be bus number 0 and be the bus directly behind the host-to-PCI bridge, but it may also be any PCI bus that is behind bus number 0 in which virtual PCI device ~~150~~ 160 appears to be behind and it is consistent with the methods of the present inventions for there to be more than one primary PCI bus in a particular system. Additionally the primary PCI bus ~~160~~ 150 may be an actual PCI bus or it may be virtual. The terms virtual and logical, for purposes of this specification, refer to the perspective of a computer program running on any one or more processor 130 where the program has an impression of a physical device or structure that may not be reflective of an actual physical device or structure. Virtual PCI device 160 logically resides on a PCI bus 151, which is either an extension of primary PCI bus 150 or one of its subordinates 156 (i.e. busses that reside behind primary PCI bus 150).